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None
*****PL

** CONTINUING DATA *****PL

Y23
** FOREIGN APPLICATIONS *****PL

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| 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | DRAWING 5 | CLAIMS 16 | CLAIMS 4 |
| Verified and Acknowledged | <i>[Signature]</i> Examiner's Signature | Initials | | | |

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TITLE

Memory device and method of amplifying voltage levels of bit line and complementary bit line

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